

#7

LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		ATTY. DOCKET NO.	APPLICATION NO.
		9818-061-999	To be assigned
		APPLICANT	
		Chang et al.	
FILING DATE	GROUP		
December 14, 2001	10/21 8990		
		To be assigned	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
KAN	AA	6,163,166	12/19/00	Bielby et al.	326	38	
KAN	AB	5,248,907	09/28/93	Lin et al.	326	4433	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AJ							
	AK							
	AL							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

KAN	AM	Kuang, J.B., et al., "PD/SOI CMOS Schmitt Trigger Circuits with Controllable Hysteresis," IBM Corporation, (April 2001)
KAN	AN	"CD4016BM/CD40106BC Hex Schmitt Triger," National Semiconductor, (February 1988)
KAN	AO	"CMOS Schmitt Trigger—A Uniquely Versatile Design Component," Fairchild Semiconductor Application Note 140 (June 1975)

EXAMINER	WELLS	DATE CONSIDERED	6/29/02
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.